

Ground:				
GndTripBk	Unblocked	Unblocked	Unblocked	Unblocked
FastTripBlock	Unblocked	Unblocked	Unblocked	Unblocked
TCCGMinTrip	10	10	10	10
TCC1GCurve	119	119	119	119
TCC1GMultEnable	Enable	Enable	Enable	Enable
TCC1GMult	1	0.3	0.3	0.3
TCC1GAddEnable	Disable	Disable	Disable	Disable
TCC1GAdd	0	0	0	0
TCC1GMRTAEnable	Disable	Disable	Disable	Disable
TCC1GMRTA	0.013	0.013	0.013	0.013
TCC1GHCTEnable	Disable	Disable	Disable	Disable
TCC1GHCT Mul	32	32	32	32
TCC1GHCTDly	0.016	0.016	0.016	0.016
TCC2GCurve	135	135	135	135
TCC2GMultEnable	Disable	Disable	Disable	Disable
TCC2GMult	1	1	1	1
TCC2GAddEnable	Disable	Disable	Disable	Disable
TCC2GAdd	0	0	0	0
TCC2GMRTAEnable	Disable	Disable	Disable	Disable
TCC2GMRTA	0.013	0.013	0.013	0.013
TCC2GHCTEnable	Disable	Disable	Disable	Disable
TCC2GHCT Mul	32	32	32	32
TCC2GHCTDly	0.016	0.016	0.016	0.016

Negative Sequence:				
NegSeqTripBlk	Blocked	Blocked	Blocked	Blocked
FastTripBlock	Unblocked	Unblocked	Unblocked	Unblocked
TCCQMinTrip	100	100	100	100
TCC1QCurve	141	141	141	141
TCC1QMultEnable	Disable	Disable	Disable	Disable
TCC1QMult	1	1	1	1
TCC1QAddEnable	Disable	Disable	Disable	Disable
TCC1QAdd	0	0	0	0
TCC1QMRTAEnable	Disable	Disable	Disable	Disable
TCC1QMRTA	0.013	0.013	0.013	0.013
TCC1QHCTEnable	Disable	Disable	Disable	Disable
TCC1QHCT Mul	32	32	32	32
TCC1QHCTDly	0.016	0.016	0.016	0.016
TCC2QCurve	117	117	117	117
TCC2QMultEnable	Disable	Disable	Disable	Disable
TCC2QMult	1	1	1	1
TCC2QAddEnable	Disable	Disable	Disable	Disable
TCC2QAdd	0	0	0	0
TCC2QMRTAEnable	Disable	Disable	Disable	Disable
TCC2QMRTA	0.013	0.013	0.013	0.013
TCC2QHCTEnable	Disable	Disable	Disable	Disable
TCC2QHCT Mul	32	32	32	32
TCC2QHCTDly	0.016	0.016	0.016	0.016

<u>Operations Sequence</u>	<u>Normal</u>	<u>Alternate 1</u>	<u>Alternate 2</u>	<u>Alternate 3</u>
Operations To LO	1	1	1	1
Phase/Neg Sequence:				
PQOper#1	TCC1	TCC1	TCC1	TCC1
PQOper#2	TCC1	TCC1	TCC1	TCC1
PQOper#3	TCC2	TCC2	TCC2	TCC2
PQOper#4	TCC2	TCC2	TCC2	TCC2
Ground:				
GndOper#1	TCC1	TCC1	TCC1	TCC1
GndOper#2	TCC1	TCC1	TCC1	TCC1
GndOper#3	TCC2	TCC2	TCC2	TCC2
GndOper#4	TCC2	TCC2	TCC2	TCC2

<u>Reclose Intervals</u>	<u>Normal</u>	<u>Alternate 1</u>	<u>Alternate 2</u>	<u>Alternate 3</u>
Phase/Neg Sequence:				
PQOpenInt#1	2	2	2	2
PQOpenInt#2	2	2	2	2
PQOpenInt#3	5	5	5	5
Ground:				
GndOpenInt#1	2	2	2	2
GndOpenInt#2	2	2	2	2
GndOpenInt#3	5	5	5	5
ResetTime	20	20	20	20

<u>Cold Load Pickup</u>	<u>Normal</u>	<u>Alternate 1</u>	<u>Alternate 2</u>	<u>Alternate 3</u>
CLPUBlock	Yes	Yes	Yes	Yes
Phase:				
CLPUPMinTrip	20	20	20	20
CLPUPCurve	165	165	165	165
CLPUPMultEnable	Disable	Disable	Disable	Disable
CLPUPMult	2	2	2	2
CLPUPAddEnable	Disable	Disable	Disable	Disable
CLPUPAdd	0	0	0	0
CLPUPMRTAEnable	Disable	Disable	Disable	Disable
CLPUPMRTA	0.013	0.013	0.013	0.013
CLPUPHCTEnable	Enable	Enable	Enable	Enable
CLPUPHCT Mul	32	32	32	32
CLPUPHCTDly	0.016	0.016	0.016	0.016
Ground:				
CLPUGMinTrip	10	10	10	10
CLPUGCurve	111	111	111	111
CLPUGMultEnable	Disable	Disable	Disable	Disable
CLPUGMult	1	1	1	1
CLPUGAddEnable	Disable	Disable	Disable	Disable
CLPUGAdd	0	0	0	0
CLPUGMRTAEnable	Disable	Disable	Disable	Disable
CLPUGMRTA	0.013	0.013	0.013	0.013
CLPUGHCTEnable	Disable	Disable	Disable	Disable
CLPUGHCT Mul	32	32	32	32
CLPUGHCTDly	0.016	0.016	0.016	0.016
Negative Sequence:				
CLPUQMinTrip	100	100	100	100
CLPUQCurve	141	141	141	141
CLPUQMmultEnable	Disable	Disable	Disable	Disable
CLPUQMmult	1	1	1	1
CLPUQAddEnable	Disable	Disable	Disable	Disable
CLPUQAdd	0	0	0	0
CLPUQMRTAEnable	Disable	Disable	Disable	Disable
CLPUQMRTA	0.013	0.013	0.013	0.013
CLPUQHCTEnable	Disable	Disable	Disable	Disable
CLPUQHCT Mul	32	32	32	32
CLPUQHCTDly	0.016	0.016	0.016	0.016

<u>Frequency</u>	<u>Normal</u>	<u>Alternate 1</u>	<u>Alternate 2</u>	<u>Alternate 3</u>
Underfrequency:				
UFreqEnable	Off	Off	Off	Off
UFreq1PU	46	46	46	46
UFreq1Time	100	100	100	100
UFreq2PU	46	46	46	46
UFreq2Time	100	100	100	100
Overfrequency:				
OFreqEnable	Off	Off	Off	Off
OFreq1PU	54	54	54	54
OFreq1Time	100	100	100	100
OFreq2PU	54	54	54	54
OFreq2Time	100	100	100	100
U/OF Tripping Supervision:				
Freq:MinVolt	3.6	3.6	3.6	3.6
UF Loadshed Restore:				
FreqRestoreEnable	Off	Off	Off	Off
Freq:81OR:PU	50.05	50.05	50.05	50.05
Freq:62Schedule	300	300	300	300
Freq:62Abort	600	600	600	600
Freq:62Transient	0.3	0.3	0.3	0.3
VoltFreqRestSupEn	Off	Off	Off	Off

RecsTime and Control

	<u>Normal</u>	<u>Alternate 1</u>	<u>Alternate 2</u>	<u>Alternate 3</u>
79ResetTarEnable	Enable	Enable	Enable	Enable
79ResetTar	2	2	2	2
79SeqCoorEnable	Disable	Disable	Disable	Disable
79SeqCoorOps	2	2	2	2

Reclose Retry

	<u>Normal</u>	<u>Alternate 1</u>	<u>Alternate 2</u>	<u>Alternate 3</u>
RecloseRetryEnable	Disable	Disable	Disable	Disable
RecloseRetryAttempts	1	1	1	1
RecloseRetryInterval	60	60	60	60

Voltage

	<u>Normal</u>	<u>Alternate 1</u>	<u>Alternate 2</u>	<u>Alternate 3</u>
Undervoltage:				
UVolt1PEnable	Off	Off	Off	Off
UVolt3PEnable	Off	Off	Off	Off
UVolt1P/3Pinhibit	Off	Off	Off	Off
UVolt1PPU	0.21	0.21	0.21	0.21
UVolt1PTime	100	100	100	100
UVolt3PPU	0.36	0.36	0.36	0.36
UVolt3PTime	100	100	100	100
Overvoltage:				
OVoltEnable	Off	Off	Off	Off
OVolt1PPU	0.24	0.24	0.24	0.24
OVolt1PTime	100	100	100	100
OVolt3PPU	0.415	0.415	0.415	0.415
OVolt3PTime	100	100	100	100
U/OV Loadshed Restore:				
VoltRestoreEnable	Off	Off	Off	Off
VoltRestoreMode	Any Single Phase	Any Single Phase	Any Single Phase	Any Single Phase
VoltRestHiL	15.12	15.12	15.12	15.12
VoltRestLoL	13.68	13.68	13.68	13.68
Freq:62Schedule	300	300	300	300
Freq:62Abort	600	600	600	600
Freq:62Transient	0.3	0.3	0.3	0.3
VoltFreqRestSupEn	Off	Off	Off	Off

Sensitive Earth Fault

<u>Normal</u>	<u>Alternate 1</u>	<u>Alternate 2</u>	<u>Alternate 3</u>
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SEFBlock	Enable	Enable	Enable	Enable
SEFMinTrip	10	10	10	10
SEFTime	1	1	1	1
SEFReclnt	2	2	2	2
SEFNumOps	1	1	1	1

<u>Directional Control</u>	<u>Normal</u>	<u>Alternate 1</u>	<u>Alternate 2</u>	<u>Alternate 3</u>
DirMTA	60	60	60	60
DirPhs	NonDirectional	NonDirectional	NonDirectional	NonDirectional
DirGnd	NonDirectional	NonDirectional	NonDirectional	NonDirectional
DirNegSeq	NonDirectional	NonDirectional	NonDirectional	NonDirectional

<u>Low Set</u>	<u>Normal</u>	<u>Alternate 1</u>	<u>Alternate 2</u>	<u>Alternate 3</u>
Phase:				
LSPEnable	Disable	Disable	Disable	Disable
LSPPU	3200	3200	3200	3200
LSPTIMEDelay	100	100	100	100
Ground:				
LSGEnable	Disable	Disable	Disable	Disable
LSGPU	1600	1600	1600	1600
LSGTIMEDelay	100	100	100	100
Negative Sequence:				
LSQEnable	Disable	Disable	Disable	Disable
LSQPU	3200	3200	3200	3200
LSQTIMEDelay	100	100	100	100

<u>Sync Check</u>	<u>Normal</u>	<u>Alternate 1</u>	<u>Alternate 2</u>	<u>Alternate 3</u>
25DV:Enable	Disable	Disable	Disable	Disable
25DV:DLDB	Disable	Disable	Disable	Disable
25DV:DLHB	Disable	Disable	Disable	Disable
25DV:HLDB	Disable	Disable	Disable	Disable
25DV:HLHB	Disable	Disable	Disable	Disable
25DV:27	15840	15840	15840	15840
25DV:27DEAD	2640	2640	2640	2640
25DV:59	12240	12240	12240	12240
25DV:59LIVE	11520	11520	11520	11520
25DV	40	40	40	40
StaticAngleDelay	10	10	10	10
MechanismOpDelay	0.1	0.1	0.1	0.1